



Geschichte

Profil

Freeware

Produkte

Shrinkware

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pi-sysprog

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[freebeone](#)

[morefreebs](#)

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[Democenter](#)

[Shrinkware](#)

[CPW](#)

[DMPbaer](#)

[DMPstack](#)

[objserv](#)

[WAITFOR](#)

[PISNMP..](#)

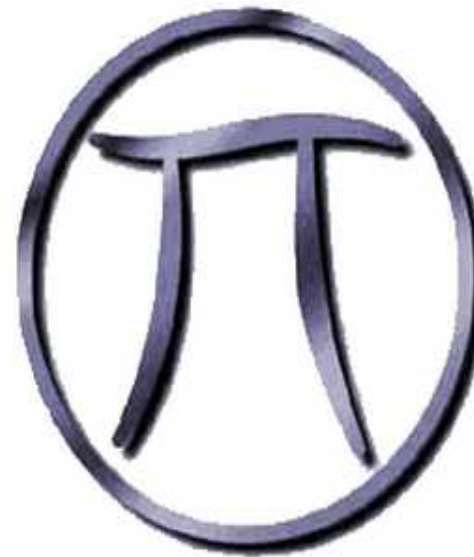


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Pi-Systemprogrammierungs-GmbH



Teichstraße 39E

63225 Langen

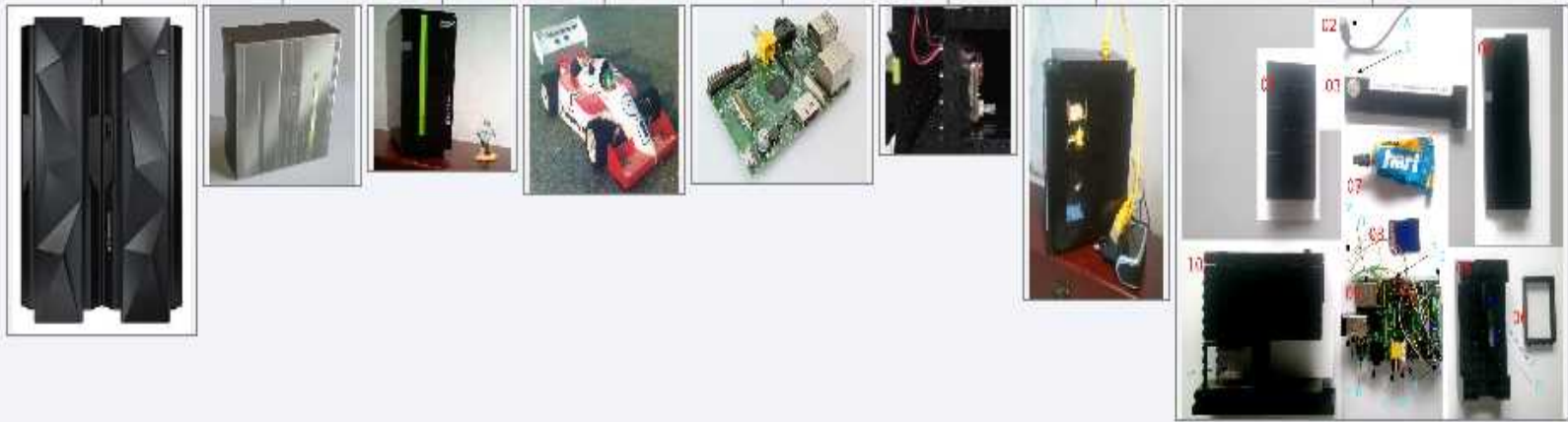
tel: 06103-71254

tagsüber: 0171-850 7132

Email: info@pi-sysprog.de



Hardware



Software



OV

Live



The new zEC12





„my“ old z10





Upgrade of the front panel





z114 in Lego





my Lego z114



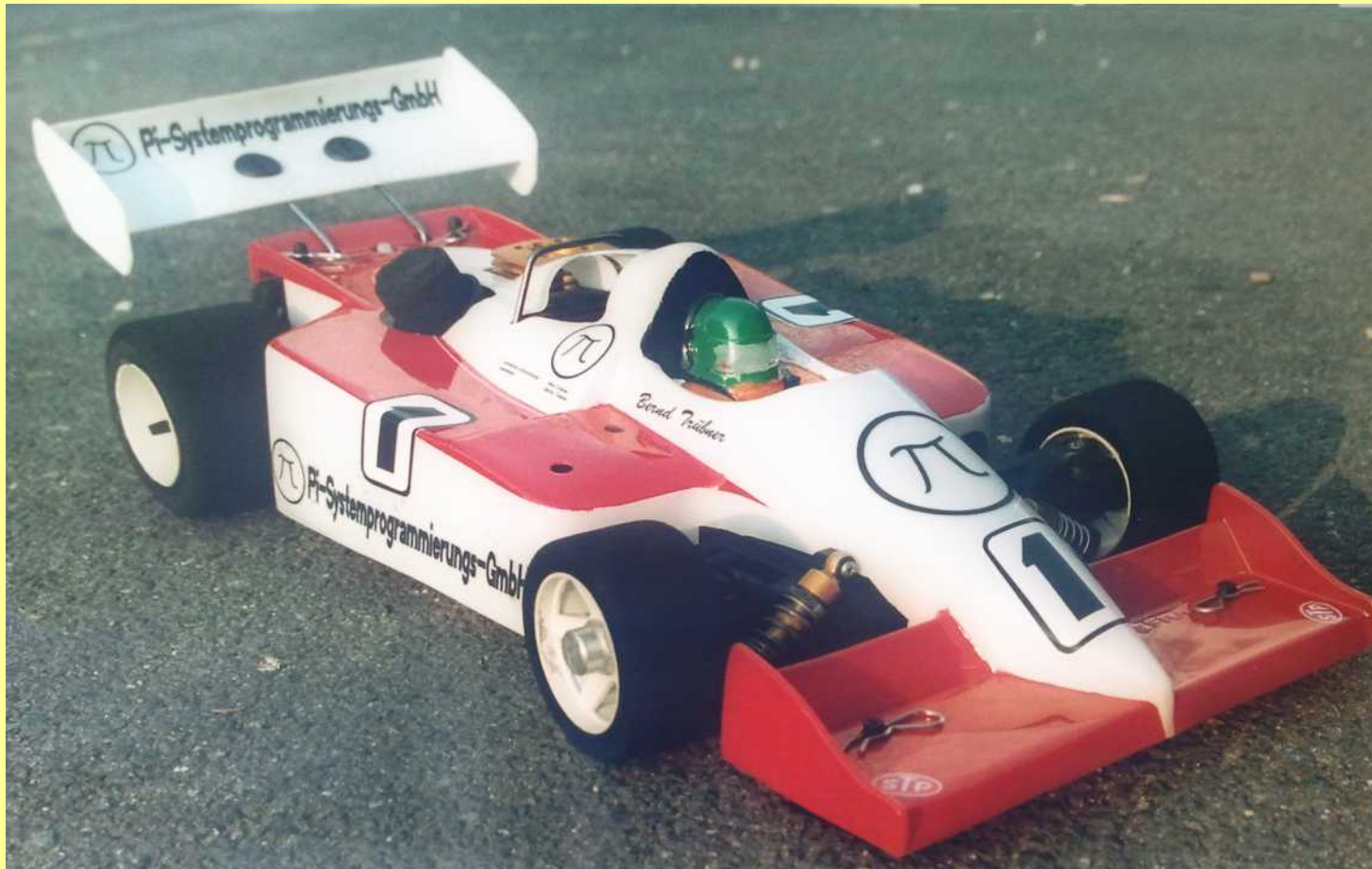


Real vs Model





An idea is born





light and cable from top



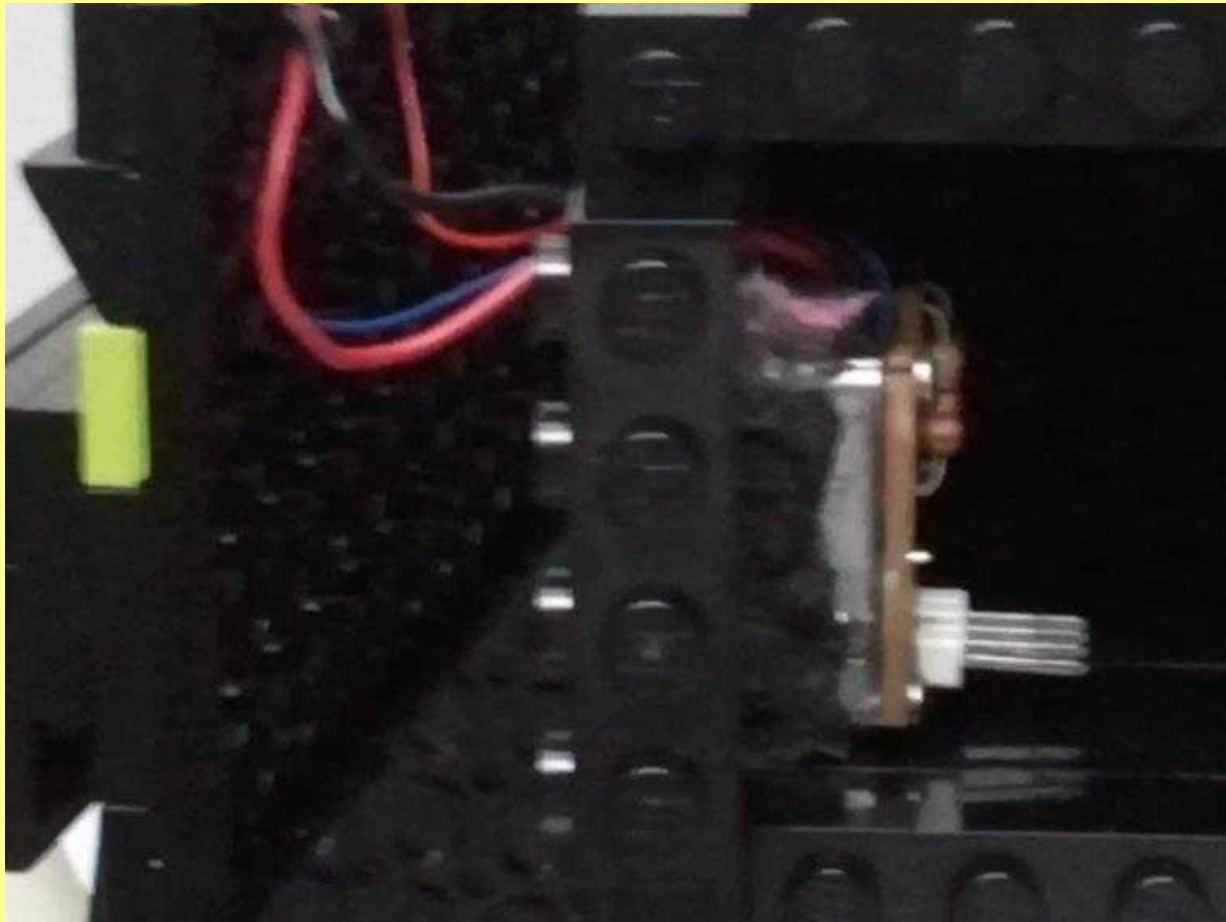


A „real“ engine





The light controller



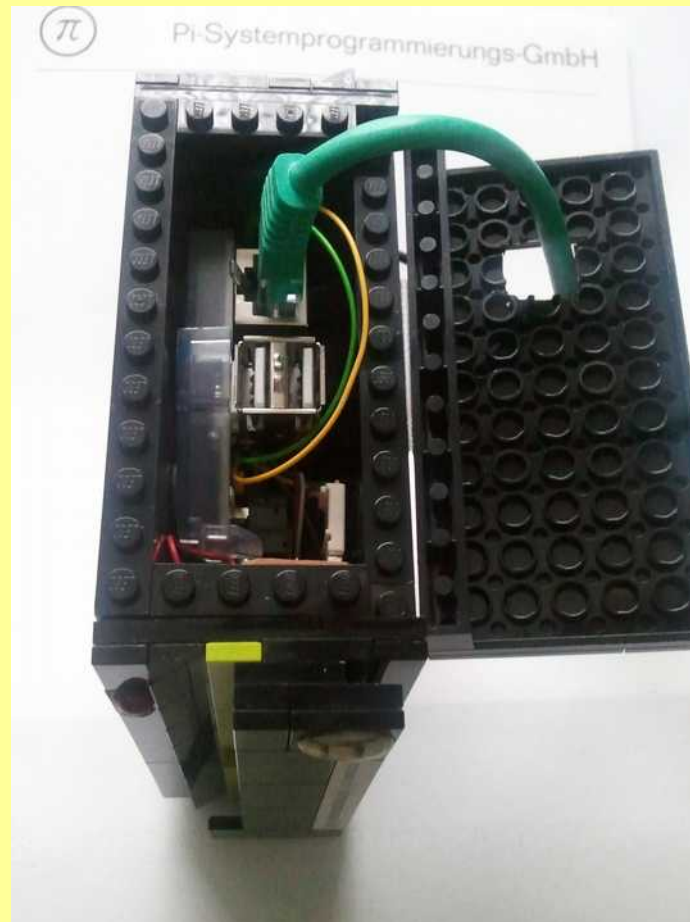


Very first version





first version; cover open



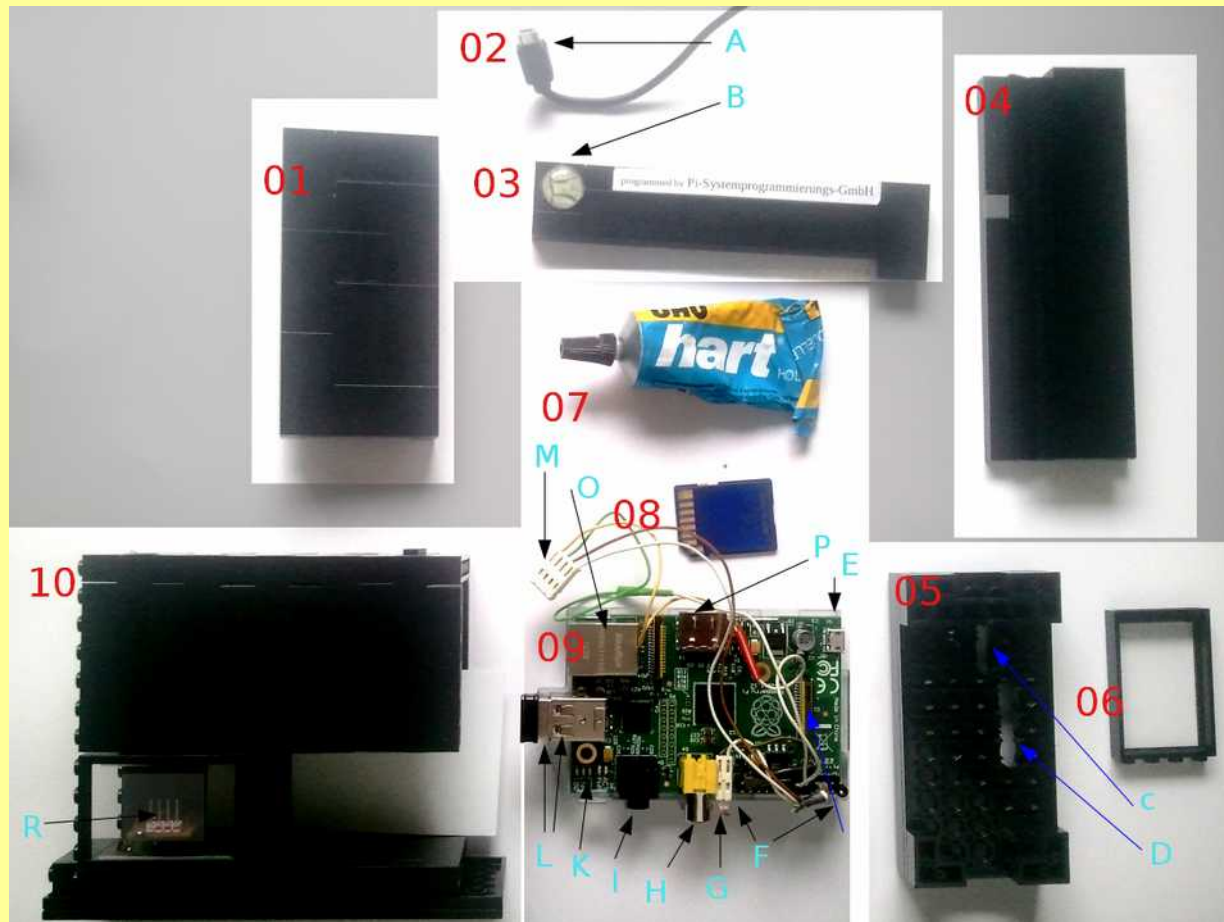


Final version (+WLAN USB)





Details (+questions re: HW)





3 Layers

- z-OP-sys (i.E. Linux on Z)
- Simulation layer
- Debian wheezy





Benchmark

AMD Athlon(tm) 7550 Dual-Core Processor

Raspberry Pi

DOS/VS R34 / under HERC 4.0 on Mageia

= / HERC 3.07 on Debian

BG 0000 Branches:

BG 0000 BCT R0,* 55.2 MIPS

4.4 MIPS

BG 0000 BCTR R0,R15 80.4 MIPS

5.7 MIPS

BG 0000 NOP R0 143.0 MIPS

8.0 MIPS

BG 0000 Fetches:

BG 0000 LR R1,R0 109.8 MIPS

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BG 0000 LTR R1,R0 118.0 MIPS

8.3 MIPS

BG 0000 L R1,0 13.9 MIPS

0.7 MIPS

BG 0000 L R1,1 13.8 MIPS

0.6 MIPS

BG 0000 ICM R1,15,0 13.6 MIPS

0.6 MIPS

BG 0000 LD F0,0 12.2 MIPS

0.6 MIPS

MIPS at 95% (COBOL-Compile) for > 5 Min

28.0

> 60 Min

0.8

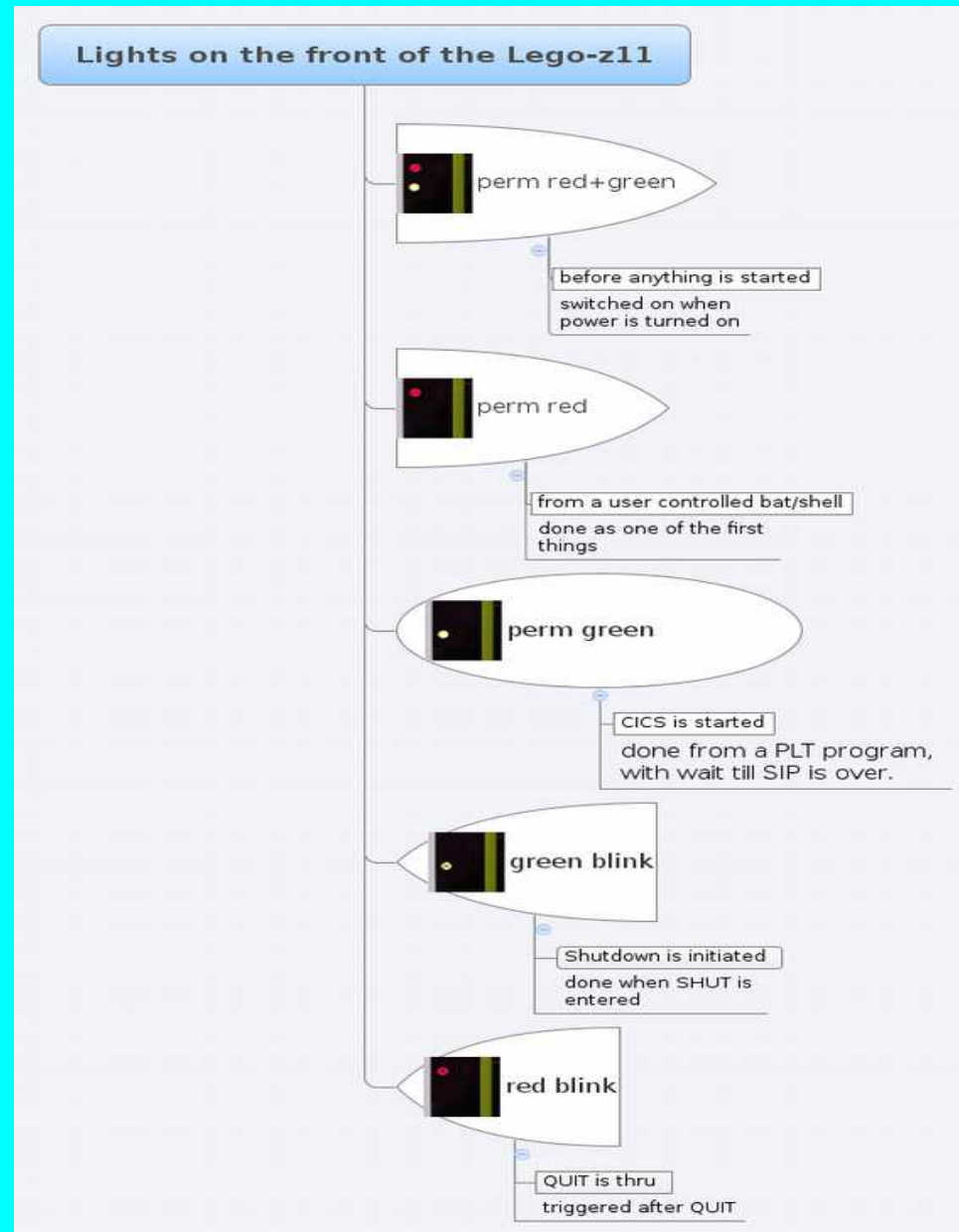


Where is the HMC?





The lights





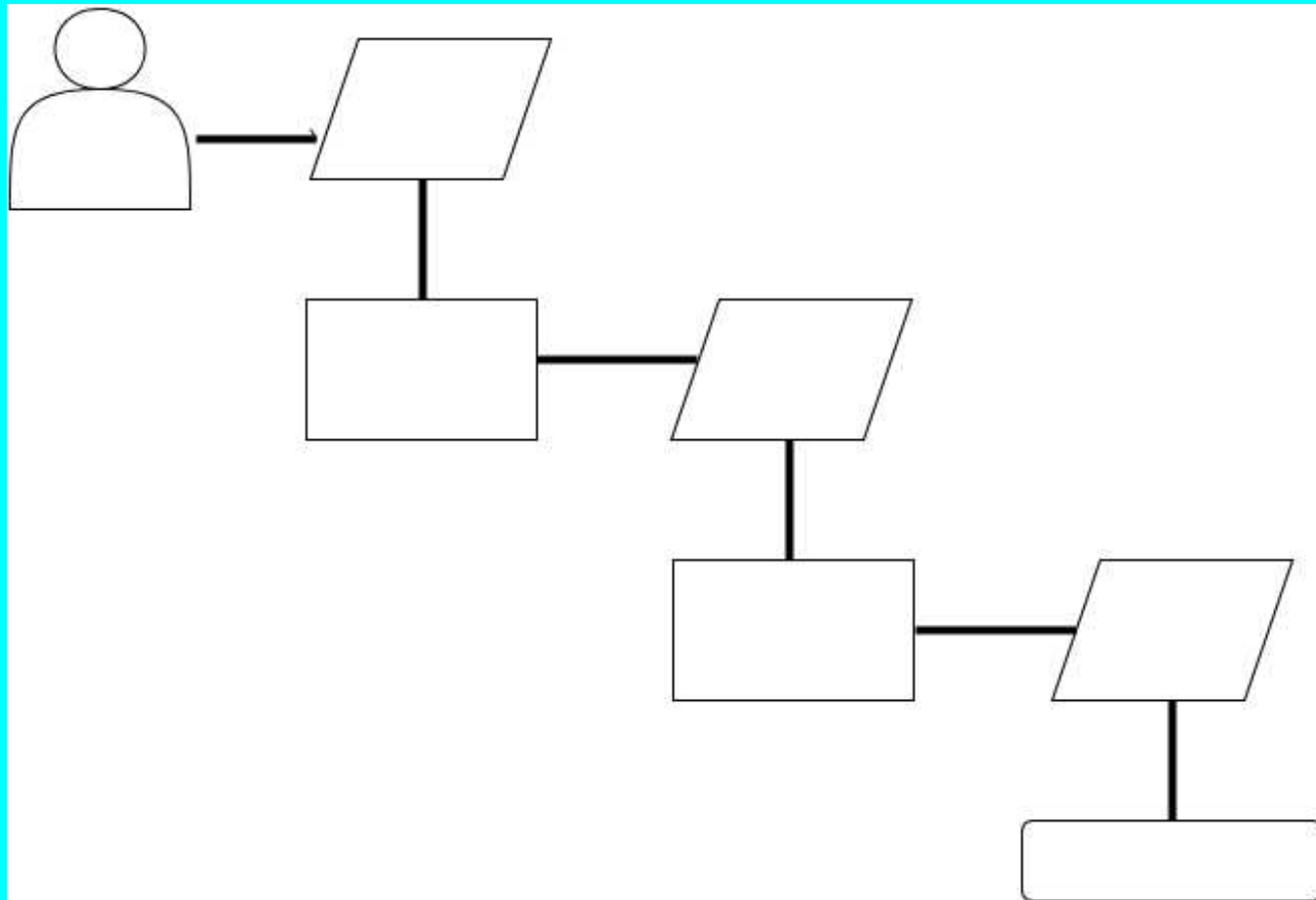
and now with WLAN





Communication

Trigger CICS Batch Host





- Next

the CEC should be up
by now



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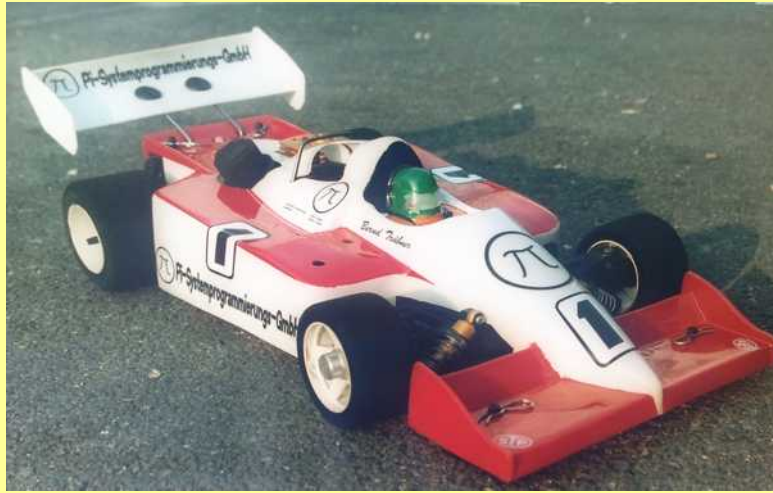


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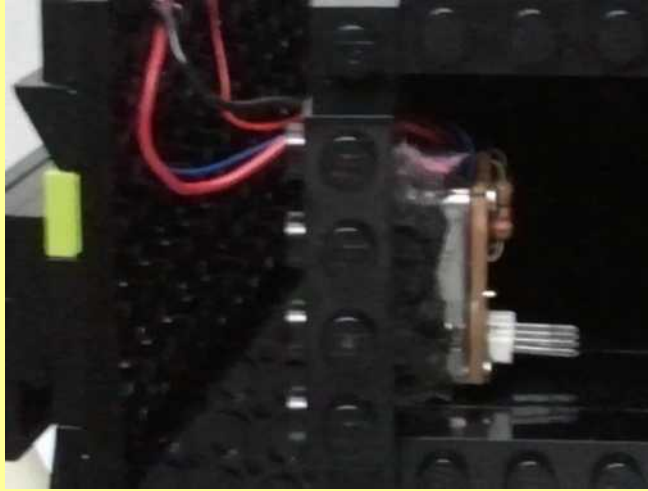


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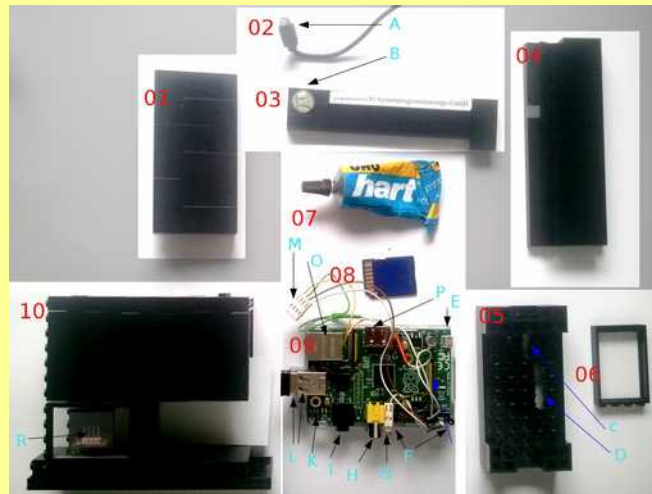


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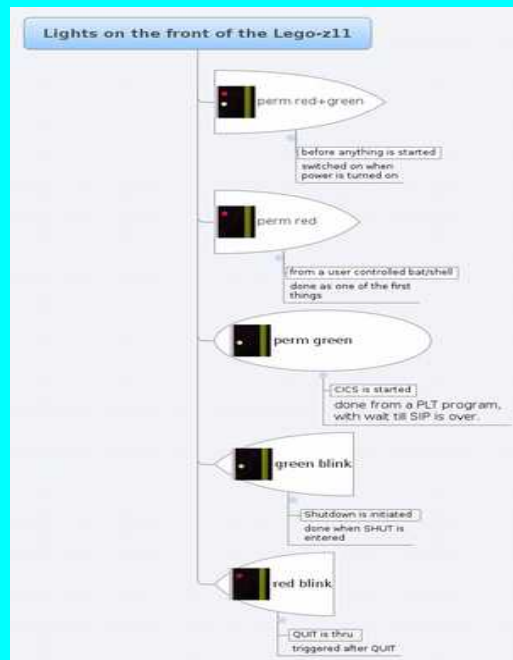


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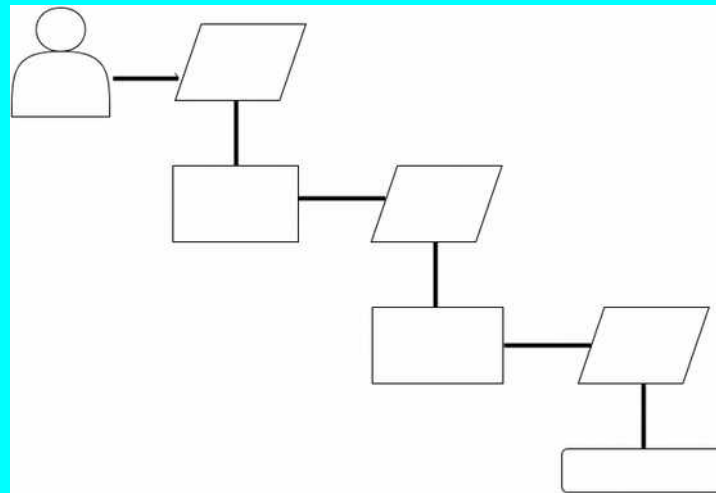
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